

IN THE SPECIFICATION

Please amend the Specification to add the following:

At page 6, after line 26, please add the following new paragraph:

Figure 2E' shows the contents of the identical hierarchical blocks 105-108. The port names on Figure 2E' correspond with the pin names on the blocks in Figure 2E.

At page 6, line 30, please delete the paragraph beginning with "Figure 3C..."

At page 6, after line 30, please add the following new paragraph:

Figures 3C1, 3C2 and 3C3 show the schematic of the bit decoding logic in integrated form. Figures 3C1, 3C2 and 3C3 are interconnected by the port connectors which connect ports of identical names, for example ACK is connected to all other connectors named ACK.